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"5276882" | USPAT;
US-PGPUB;
EPO; JPO | 2004/02/22 09:25 |

–	2081	713/200.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:29
–	475	711/152.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:34
–	1125	711/170.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:35
–	1	713/200.ccls. and (711/152.ccls. and 711/170.ccls.)	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:35
–	24	713/200.ccls. and (711/152.ccls. 711/170.ccls.)	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:44
–	0	711/152.ccls. and (stack with (memory near2 protect$3))	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:45
–	2	711/152.ccls. and (stack same (memory with protect$3))	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:47
–	5	711/170.ccls. and (stack same (memory with protect$3))	USPAT; US-PGPUB; EPO; JPO	2004/02/22 09:52
–	12	713/200.ccls. and (stack same (memory with protect$3))	USPAT; US-PGPUB; EPO; JPO	2004/02/22 10:02
–	1163334	stack memory protection.ti.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 10:02
–	0	"stack memory protection".ti.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 10:02
–	0	"stack memory protection".ab.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 10:03
–	14	(stack same (memory with protection)).ab.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 10:03

L Number	Hits	Search Text	DB	Time stamp
-	15	("intel architecture" "intel architecure 64" "IA64").ab.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 12:51
-	9	("intel architecure 64" "IA-64" "IA64").ab.	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:02
-	2	(stack same (memory with protection)) and ("IA-64" "IA64")	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:07
-	0	((stack with memory) and ("IA-64" "IA64")) and @py<2000	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:08
-	0	((stack with memory) and ("IA-64" "IA64")) and @py<2002	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:08
-	5	((stack same memory) and ("IA-64" "IA64")) and @py<2002	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:08
-	45	(stack same memory) and ("IA-64" "IA64")	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:08
-	16	((stack same memory) and ("IA-64" "IA64")) and protect$3	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:09
-	3	((stack same memory) and ("IA-64" "IA64")) and (stack same (memory with protect$3))	USPAT; US-PGPUB; EPO; JPO	2004/02/22 13:09

L Number	Hits	Search Text	DB	Time stamp
1	2	713/200.ccls. and "load/store instructions"	USPAT	2004/02/23 12:28
2	3	713/200.ccls. and "load/store"	USPAT	2004/02/23 12:29
3	1	713/200.ccls. and (stack same (memory near5 protection))	USPAT	2004/02/23 12:32
4	2	713/200.ccls. and (stack same (memory with protection))	USPAT	2004/02/23 12:33
5	684	713/200.ccls. and @py<2001	USPAT	2004/02/23 12:33
6	5	(713/200.ccls. and @py<2001) and ((program process) near8 "memory protection")	USPAT	2004/02/23 12:34